JRC

# 3-INPUT 1-OUTPUT VIDEO SWITCH

#### GENERAL DESCRIPTION

The NJM2535 is a video switch for VCR, TV and others. It contains three cramp-type inputs and one buffer-type output.

- FEATURES
- Operating Voltage .
- Low Operating Current
- Crosstalk .
- 3-Input, 1-Output
- Bipolar Technology
- Package Outline

 $(+4.5V \sim +13V)$ (4.6mA MAX) (-70dB)

DIP8, DMP8, SIP8, SSOP8



PACKAGE OUTLINE



NJM2535D

NJM2535M

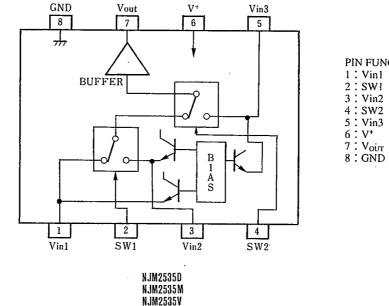




NJM2535L

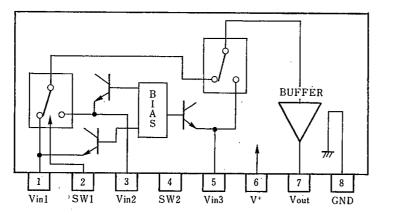


#### PIN CONFIGURATION



PIN FUNCTION

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PIN FUNCTION 1:Vinl 2:SW1 2 . Sw1 3 : Vin2 4 : SW2 5 : Vin3 6 : V<sup>+</sup> 7 : V<sub>OUT</sub> 8 : GND



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ABSOLUTE MAXIMUM RATINGS			(Ta=25℃)	
PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V <sup>+</sup>	+15	v	
Power Dissipation		(DIP-8) 500		
		(DMP-8) 300	mW	
	PD	(SIP-8) 800		
		(SSOP-8) 250		
Operating Temperature Range	T <sub>opr</sub>	-20~+75	Ĉ	
Storage Temperature Range	T <sub>stg</sub>	-40~+125	°C	

## **ELECTRICAL CHARACTERISTICS**

(V<sup>+</sup>=5V, Ta=25℃)

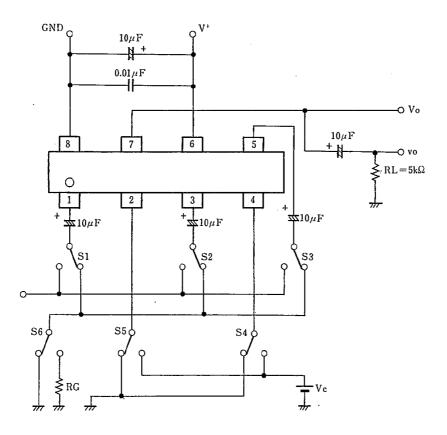
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V*		+4.5	_	+13.0	v
Supply Current	I <sub>CC</sub>		-	3.6	4.6	mA
Frequency Characteristics	Gf	V <sub>IN</sub> =2Vpp, Vo=10MHz/100kHz	-1.0	0	+1.0	dB
Voltage Gain	· Gv	V <sub>IN</sub> =2Vpp, 100kHz	-0.5	0	+0.5	dB
Differential Gain	DG	V <sub>IN</sub> =2Vpp, Standard staircase signal, APL=50%	-	0	3.0	%
Differential Phase	DP	V <sub>IN</sub> =2Vpp, Standard staircase signal, APL=50%	-	0	3.0	deg
Output Offset Voltage	V <sub>off</sub>	· ·	-30	0	+30	mV
Crosstalk	СТ	V <sub>IN</sub> =2Vpp, 4.3MHz		-70	-60	dB
Switching Voltage	V <sub>CH</sub>		2.4	-	-	v
	V <sub>CL</sub>		_	-	0.8	v
Input Impedance	RI		—	30	-	kΩ
Output Impedance	Ro		-	25	_	Ω
Input Bias Voltage	V <sub>IN</sub>		_	2.5	-	v

### ■ INPUT CONTROL SIGNAL OUTPUT SIGNAL

SW1	SW2	OUTPUT SIGNAL	
L	L	V <sub>IN</sub> 1	
Н	L	V <sub>IN</sub> 2	
L/H	Н	V <sub>IN</sub> 3	

NJM2535

TEST CIRCUIT



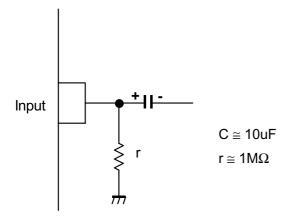
This IC requires  $1M\Omega$  resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.

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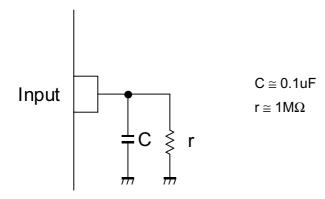
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# ■APPLICATION

This IC requires  $1M\Omega$  resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



This IC requires 0.1uF capacitor between INPUT and GND,  $1M\Omega$  resistance between INPUT and GND for clamp type input at mute mode.



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